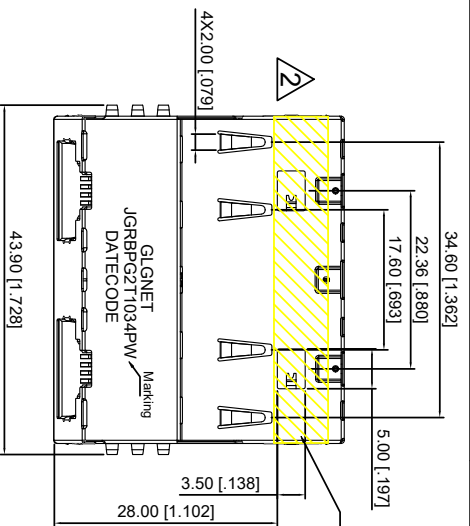
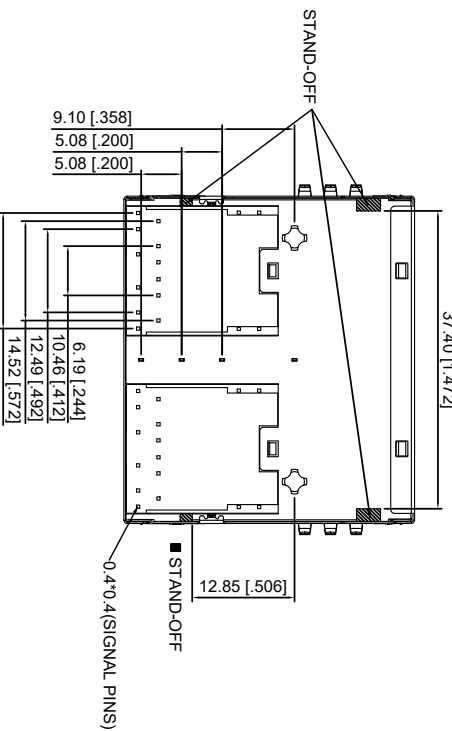
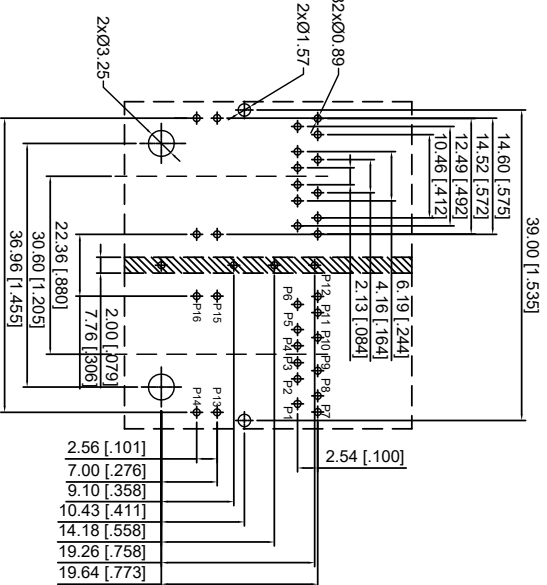
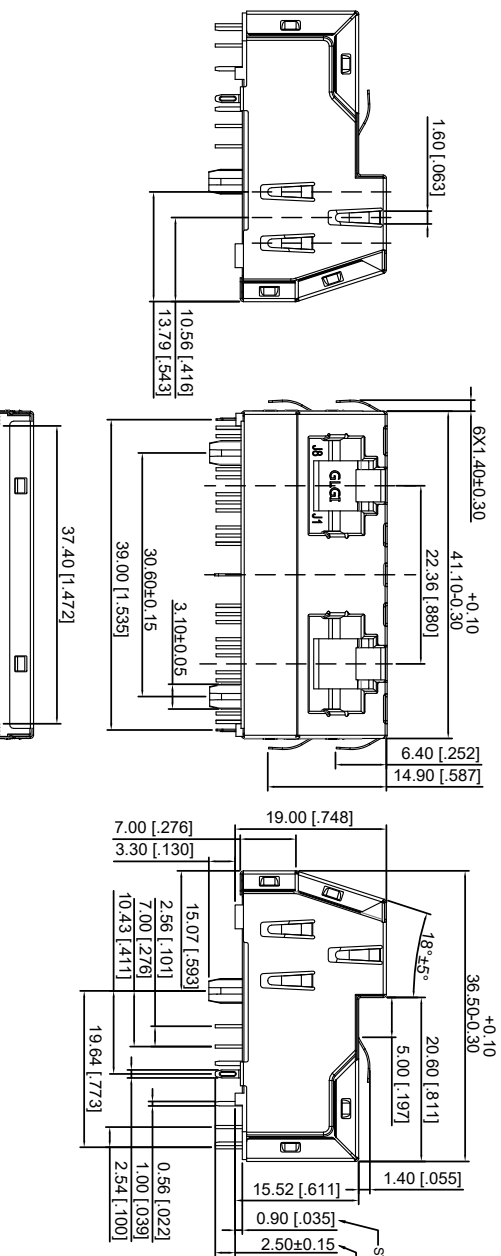


NO.	ECN NO.	DESCRIPTION	REV.	DRAW	DATE
1		1.刷新SI参数,按作为研发要求	A1	DANNY	09.19.2019
2		1.申请PCN,顶部增加两个接地开孔 2.增加麦拉贴合,封闭两个开孔	A2	DANNY	11.20.2019



Goldplating Option	
1.8μ"	<input type="checkbox"/>
3μ"	<input type="checkbox"/>
6μ"	<input type="checkbox"/>
15μ"	<input type="checkbox"/>
30μ"	<input type="checkbox"/>
50μ"	<input type="checkbox"/>



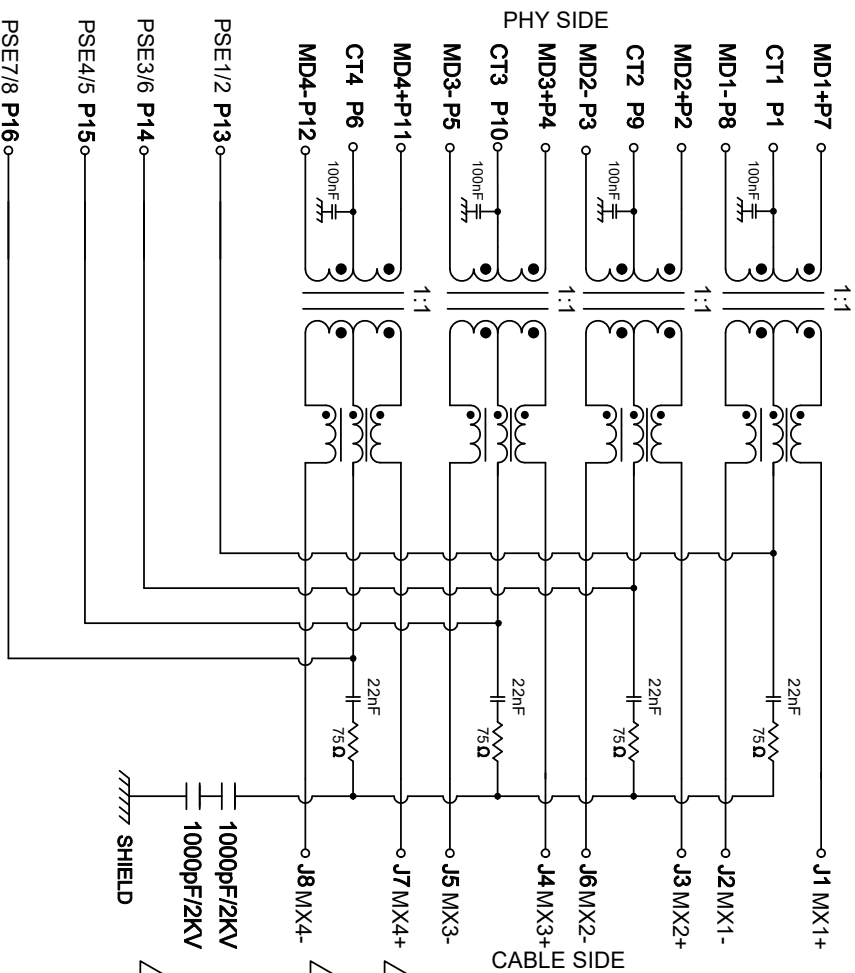
RECOMMENDED P.C.B LAYOUT
TOP VIEW (COMPONENT SIDE)

Dimensions Tolerance:
All PCB LAYOUT
Tolerances:±0.05[002]

Description		料号		设计	
RJ45 1X2 Tab_Up Through Hole/4PPOE(120W) 10G Base-T/Extended Temp W/O LED		44482-60M231		WANGKG	
Dimension Tolerance: 0.xx:±0.10[004] x.xx:±0.25[010] xx.xx:±0.25[010]		品名 TITLE		校对 CHECK	
比例 SCALE 1:1		客户 CUSTOMER		批准 APPROVAL	
张数 SHEET 1/2		图号 DWG NO.		日期 DATE	
备注 REMARK		角法 PROJ.		版本 REV	

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SHEN ZHEN GLGNET ELECTRONICS CO.LTD.

Schematic



MATERIALS

1.HOUSING:GLASS FILLED LCP UL94V-0(RHHS)
2.CONTACT:PHOSPHOR BRONZE 50-100MICRONCHES MIN. OVERALL DUCTILE NICKEL UNDERPLATEWITH SELECTED 30 MICRONCHES GOLD AT MATING INTERFACE
3.SHIELD:0.2mm THICKNESS WITH COPPER ALLOY.
4.JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68 SUBPART F
5.THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS (OR THROUGH HOLE REFLOW SOLDERING PROCESS) PEAK SOLDERING TEMPERATURE IS 265±5°C, 10 SECS.
6.OPERATING TEMPERATURE:-40°C TO +85°C. STORAGE TEMPERATURE:-40°C TO +85°C.

NO.	ECN NO.	DESCRIPTION	REV.	DRAW	DATE
1		1.刷新SI参数,按作为研发要求	A1	DANNY	09.19.2019
2		1.申请PCN,顶部增加两个接地开孔 2.增加麦拉贴合,封闭两个开孔	A2	DANNY	11.20.2019

PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	-40°C To +85°C
TURNS RATIO	1:1±2%
INDUCTANCE(OCL)	120uH MIN@100KHz/100nV With 19mA DC Bias For(Channel1/2/3/4)
INSERTION LOSS	-1.5dBMAX@0.1-1MHz; -0.5dBMAX@1.65MHz; -0.8dBMAX@65-125MHz; -2.2dBMAX@125-400MHz; -4.5dBMAX@400-500MHz;
RETURN LOSS	-22dBMIN@1-100MHz; -24+20.75xLOG(f/100)dBMIN@100-200MHz; -12dBMIN@200-300MHz; -9.5dBMIN@300-400MHz; -7.5dBMIN@500MHz
CROSSTALK (ADJACENT CHANNELS)	(±FCC) -47dBMIN@1-100MHz; -37dBMIN@100-400MHz; -30dBMIN@400-500MHz; (±FCC) -30dBMIN@1-100MHz; -22dBMIN@100-400MHz; -20dBMIN@400-500MHz;
CROSSTALK (PORT TO PORT)	-70+10LOG(f/100)dBMIN@1-100MHz; -70+15LOG(f/100)dBMIN@100-500MHz;
COMMON MODE REJECTION RADIO	-30dBMIN@1-100MHz; -25dBMIN@100-300MHz; -20dBMIN@300-500MHz;
COMMON MODE TO DIFFERENTIAL MODE ATTENUATION	-35dBMIN@1-100MHz; -30dBMIN@100-300MHz; -25dBMIN@300-500MHz;
COMMON MODE RETURN LOSS (SCC22/PHY SIDE)	-1.5~ -5dBMIN@2-20MHz; -1.5~ -5dBMIN@250-400MHz;
DC CURRENT/VOLTAGE RATING-PSE PINS	1.15A MAXIMUM@57VDC(CONTINUOUS)
HI-POT	2250VDC@60 SECONDS(Channel1/2/3/4)

Description		料号		客户		日期	
RJ45 1X2 Tab_Up Through Hole/4PPOE(120W)	10G Base-T/Extended Temp W/O LED	44482-80M231	JGRBPG2T1034PW	11/20/2019	11/20/2019	WANGKG	DANNY
Dimension Tolerance:	0.xx:±0.10[.004]						
	x.xx:±0.25[.010]						
	xx.xx:±0.25[.010]						
比例 SCALE	1:1	单位 UNIT	mm	图号 DWG NO.	角法 PROJ.	版本 REV	A2
张数 SHEET	2/2	备注 REMARK					

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