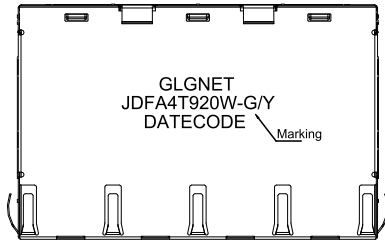


Mechanical

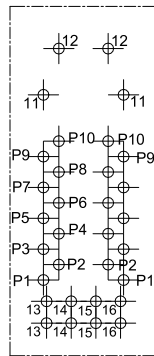
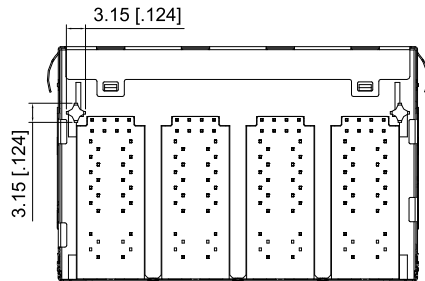
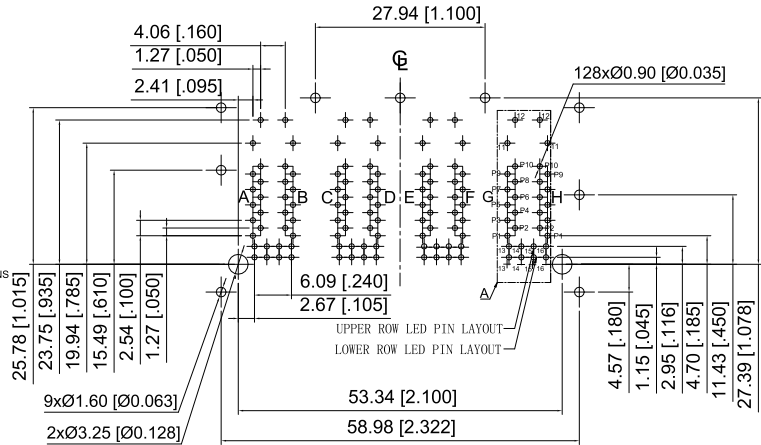
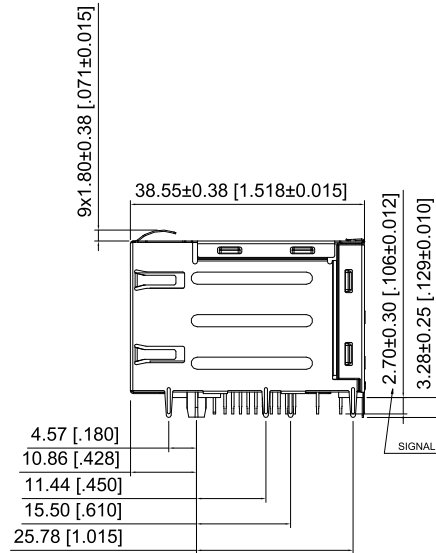
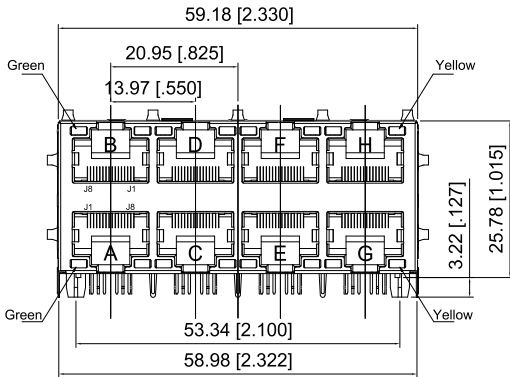
NO.	ECN NO.	DESCRIPTION	REV.	DRAW	DATE
1					
2					
3					



GoldPlating	Option
GF	<input type="checkbox"/>
3μ"	<input type="checkbox"/>
6μ"	<input type="checkbox"/>
15μ"	<input type="checkbox"/>
30μ"	<input checked="" type="checkbox"/>
50μ"	<input type="checkbox"/>

MATERIALS

- HOUSING: GLASS FILLED PA6T(BLACK) UL94V-0(RoHS)
- CONTACT: PHOSPHOR BRONZE, 50 MICROINCHES MIN. OVERALL DUCTILE NICKEL UNDERPLATE WITH SELECTED 30μ" GOLD AT MATING INTERFACE
- SHIELD: 0.2mm THICKNESS WITH BRASS.
- JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68 SUBPART F
- THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS PEAK SOLDERING TEMPERATURE IS 260°C MAX. 10 SECS MAX.
- OPERATING TEMPERATURE: -40°C TO +85°C.
STORAGE TEMPERATURE: -40°C TO +85°C.



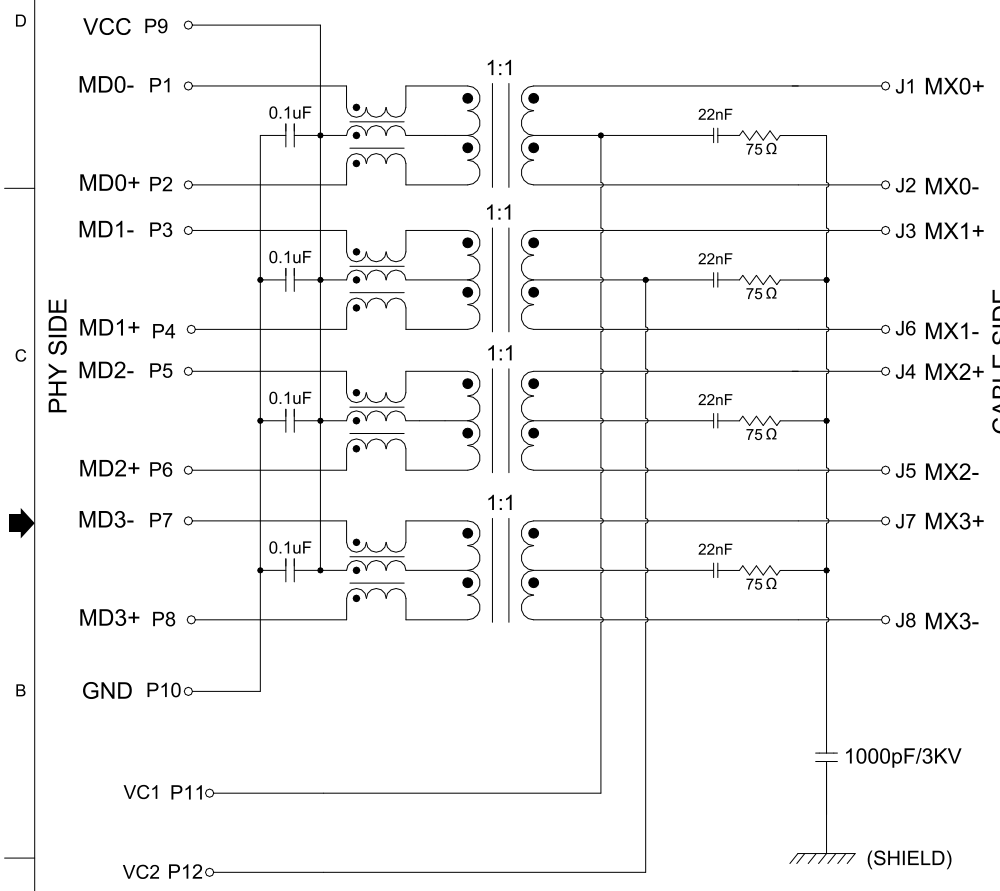
RECOMMENDED P.C.B LAYOUT
TOP VIEW (COMPONENT SIDE)

Dimension Tolerance:
PCB LAYOUT Tolerances: ±0.05[.002]

产品客户图 Customer Drw		GLGNET® 深圳市方向电子股份有限公司 SHEN ZHEN GLGNET ELECTRONICS CO., LTD.			
未注公差 TOLERANCE	0.XX ±0.10 X.XX ±0.25	Description	RJ45 2X4/Through Hole/PoE+(30W) 1000Base-T W/LED		
UNLESS OTHERWISE SPECIFIED	XX.XX ±0.25 X° ±2°		品名 TITLE	JDF44T920W-G/Y	
比例 SCALE	1:1	设计 DESIGN	WANGKG	2024.04.18	料号 ITEM NO.
张数 SHEET	1/2	大小 SIZE	A4	校对 CHECKED	DANNY
单位 UNIT	mm	版本 REV.	A0	核准 APPROVED	BYANG
				图号 DWG NO.	130113-00211

NO.	ECN NO.	DESCRIPTION	REV.	DRAW	DATE
1					
2					
3					

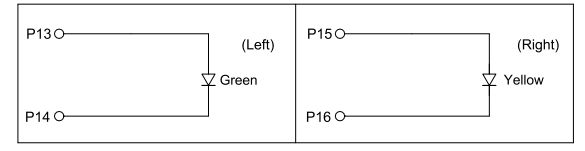
Schematic



PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	-40°C To +85°C
URNS RATIO	1:1±2%
OPEN CIRCUIT INDUCTANCE(OCL)	160uH MIN@100KHz/100mV With 15mA DC Bias For(CHANNEL1-2) 350uH MIN@100KHz/100mV With 8mA DC Bias For(CHANNEL1-4)
INSERTION LOSS(IL)	-1.2dB MAX@0.3MHz-100MHz;
RETURN LOSS(RL) (Z out=100 OHM)	-18dB MIN@1MHz-30MHz; -16dB MIN@30MHz-60MHz; -12dB MIN@60MHz-80MHz;-10dB MIN@80MHz-100MHz;
CROSSTALK (ADJACENT CHANNELS)	-30dB MIN@1MHz-100MHz;
COMMON MODE REJECTION RATIO(CMRR)	-30dB MIN@1MHz-100MHz;
DC CURRENT/VOLTAGE RATING-PSE PINS	720mA MAXIMUM@57VDC(CONTINUOUS)
HI-POT	2250 VDC@60 SECONDS

LED SPECIFICATION			
STANDARD LED	WAVELENGTH	Forward V(max)	TYP
GREEN	570nm	2.6V	2.1V
YELLOW	590nm	2.5V	2.1V

*WITH A FORWARD CURRENT OF 20mA



产品客户图 Customer Drw		GLGNET® 深圳市方向电子股份有限公司 SHEN ZHEN GLGNET ELECTRONICS CO.,LTD.			
未注公差 TOLERANCE	0.XX ±0.10 X.XX ±0.25 UNLESS OTHERWISE SPECIFIED	XX.XX ±0.25 X° ±2°	Description	RJ45 2X4/Through Hole/PoE+(30W) 1000Base-T W/LED	
比例 SCALE	1:1	角法 PROJ.	设计 DESIGN	WANGKG	2024.04.18
张数 SHEET	2/2	大小 SIZE	校对 CHECKED	DANNY	2024.04.18
单位 UNIT	mm	版本 REV.	核准 APPROVED	BYANG	2024.04.18
			品名 TITLE	JDF4T920W-G/Y	
			料号 ITEM NO.	130113-00211	
			图号 DWG NO.		